# EAST Search History

| Ref # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
|---|---|---|---|---|---|---|
| S1 | 1837 | 703/2.ccls. | US-PGPUB; USPAT | OR | OFF | 2006/06/23 10:10 |
| S2 | 11 | S1 and DAG | US-PGPUB; USPAT | OR | OFF | 2006/06/23 10:50 |
| S3 | 1049 | DAG and Cyclic$4 | US-PGPUB; USPAT | OR | OFF | 2006/06/23 11:01 |
| S4 | 1 | "5825651".pn. | US-PGPUB; USPAT | OR | OFF | 2006/06/23 11:02 |
| S5 | 220 | 700/103.ccls. | US-PGPUB; USPAT | OR | OFF | 2006/06/23 11:02 |
| S6 | 38 | ("4796194" \| "5019961" \| "5019992" \| "5355317" \| "5357440" \| "5586052" \| "5659478").PN. OR ("5825651").URPN. | US-PGPUB; USPAT; USOCR | OR | OFF | 2006/06/23 11:27 |
| S7 | 132 | combin$5 with DAG | US-PGPUB; USPAT; USOCR | OR | OFF | 2006/06/23 11:53 |
| S8 | 817 | 703/1.ccls. | US-PGPUB; USPAT; USOCR | OR | OFF | 2006/06/23 12:55 |
| S9 | 1 | "5996114".pn. | US-PGPUB; USPAT; USOCR | OR | OFF | 2006/06/23 12:40 |
| S10 | 0 | (configration adj rule) | US-PGPUB; USPAT; USOCR | OR | OFF | 2006/06/23 12:44 |
| S11 | 2 | US-6003012-$.DID. OR US-6009406-$.DID. | US-PGPUB; USPAT; USOCR | OR | OFF | 2006/06/23 12:47 |
| S12 | 19 | ("5630025" "6083267" "5515524" "5708798" "5295067" "4847761" "6216109" "5216612" "5960422" "5311424" "5796614" "6314422" "5806069" "5598511" "4939668" "4700317" "6002854" "5329464" "4831546").pn. | US-PGPUB; USPAT; USOCR | OR | OFF | 2006/06/23 14:10 |
| S15 | 1667 | combin$4 with product with (model instance) | US-PGPUB; USPAT; USOCR | OR | ON | 2006/06/23 12:55 |
| S16 | 5 | S15 and DAG | US-PGPUB; USPAT; USOCR | OR | ON | 2006/06/23 12:57 |
| S17 | 26 | configurat$4 with (DAG (Directed adj cyclic adj graph)) | US-PGPUB; USPAT; USOCR | OR | ON | 2006/06/23 12:59 |

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|---|---|---|---|---|---|---|
| S18 | 66 | (join$5 intersect$4 union disjunction) with (DAG (Directed adj cyclic adj graph)) | US-PGPUB; USPAT; USOCR | OR | ON | 2006/06/23 13:14 |
| S19 | 19 | (inconsistan$6 error (non adj combina$4) incompatibl$4) with (DAG (Directed adj cyclic adj graph)) | US-PGPUB; USPAT; USOCR | OR | ON | 2006/06/23 13:14 |
| S20 | 5 | S18 and S19 | US-PGPUB; USPAT; USOCR | OR | ON | 2006/06/23 13:01 |
| S21 | 6 | S19 and (fix$4 correct$4 remed$4 solv$4) with (inconsistan$6 error (non adj combina$4) incompatibl$4) | US-PGPUB; USPAT; USOCR | OR | ON | 2006/06/23 13:07 |
| S22 | 4 | S18 and (fix$4 correct$4 remed$4 solv$4) with (inconsistan$6 error (non adj combina$4) incompatibl$4) | US-PGPUB; USPAT; USOCR | OR | ON | 2006/06/23 13:07 |
| S26 | 1 | (US-20020165701-$).did. | US-PGPUB | OR | OFF | 2006/06/23 13:32 |
| S27 | 389 | (consolidat$4 with model$4) | US-PGPUB | OR | OFF | 2006/06/23 13:13 |
| S28 | 81 | (join$5 intersect$4 union disjunction) with (DAG (Directed adj acyclic adj graph)) | US-PGPUB; USPAT; USOCR | OR | ON | 2006/06/23 14:25 |
| S29 | 24 | (inconsistan$6 error (non adj combina$4) incompatibl$4) with (DAG (Directed adj acyclic adj graph)) | US-PGPUB; USPAT; USOCR | OR | ON | 2006/06/23 13:14 |
| S31 | 1 | S26 and (correct$4 fix$4 remed$4) | US-PGPUB | OR | OFF | 2006/06/23 13:23 |
| S32 | 0 | S26 and (rule with incompatib$7) | US-PGPUB | OR | OFF | 2006/06/23 13:23 |
| S34 | 0 | DAG and (rule with incompatib$7) | US-PGPUB; USPAT | OR | OFF | 2006/06/23 13:24 |
| S36 | 0 | DAG and (rule with inconsistant) | US-PGPUB; USPAT | OR | OFF | 2006/06/23 13:25 |
| S37 | 2 | DAG and (rule with (incompatib$6 inconsistant)) | US-PGPUB; USPAT | OR | ON | 2006/06/23 13:26 |
| S38 | 22054 | (detect$4 identify$4) with (rule inquality inconsist$8 incompatib$8) | US-PGPUB; USPAT | OR | ON | 2006/06/23 13:29 |
| S39 | 282 | S38 and (DAG (directed with acyclic with graph)) | US-PGPUB; USPAT | OR | ON | 2006/06/23 13:30 |
| S40 | 110 | (detect$4 identify$4) with (rule) with (inquality inconsist$8 incompatib$8) | US-PGPUB; USPAT | OR | ON | 2006/06/23 13:30 |
| S41 | 1 | S40 and (DAG (directed with acyclic with graph)) | US-PGPUB; USPAT | OR | ON | 2006/06/23 13:30 |
| S42 | 1 | S26 and (inconsist$8 incompatib$8) | US-PGPUB | OR | OFF | 2006/06/23 13:34 |
| S43 | 0 | "6009406".pn. | US-PGPUB | OR | OFF | 2006/06/23 13:34 |

# EAST Search History

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|---|---|---|---|---|---|---|
| S44 | 1 | "6009406".pn. | US-PGPUB; USPAT | OR | OFF | 2006/06/23 13:37 |
| S45 | 44 | (correct$4 with DAG) | US-PGPUB; USPAT | OR | OFF | 2006/06/23 13:37 |
| S46 | 12 | US-5515524-$.DID. OR US-5523942-$.DID. OR US-5825651-$.DID. OR US-5873081-$.DID. OR US-5996090-$.DID. OR US-6167383-$.DID. OR US-6192355-$.DID. OR US-6230200-$.DID. OR US-6247128-$.DID. OR US-6300948-$.DID. OR US-6343313-$.DID. OR US-6430531-$.DID. | US-PGPUB; USPAT; USOCR | OR | OFF | 2006/06/23 14:20 |
| S47 | 44 | intersecting with rule with set | US-PGPUB; USPAT; USOCR | OR | OFF | 2006/06/23 14:25 |
| S48 | 12 | graph with rule with intersect$4 | US-PGPUB; USPAT; USOCR | OR | OFF | 2006/06/23 14:21 |
| S49 | 258 | (DAG (Directed adj acyclic adj graph)) and (combin$4 with (rule model)) | US-PGPUB; USPAT; USOCR | OR | ON | 2006/06/23 14:26 |
| S50 | 59 | (DAG (Directed adj acyclic adj graph)) and (combin$4 adj2 (rule model)) | US-PGPUB; USPAT; USOCR | OR | ON | 2006/06/23 14:26 |

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**Inventor Name Search Result**

Your Search was:

Last Name = BECK
First Name = BRANDON

| Application# | Patent# | Status | Date Filed | Title | Inventor Name |
|---|---|---|---|---|---|
| 10827078 | Not Issued | 30 | 04/19/2004 | Consolidation of product data models | BECK, BRANDON M. |
| 10957919 | Not Issued | 30 | 10/04/2004 | Complex configuration processing using configuration sub-models | BECK, BRANDON M. |
| 11034141 | Not Issued | 30 | 01/12/2005 | Attribute prioritized configuration using a combined configuration-attribute data model | BECK, BRANDON M. |
| 11033915 | Not Issued | 30 | 01/12/2005 | Steerable sheath | BECK, BRANDON N. |
| 60536588 | Not Issued | 159 | 01/15/2004 | Steerable sheath | BECK, BRANDON N. |
| 60715613 | Not Issued | 20 | 09/12/2005 | Compression staple | BECKENDORF, BRANDON |
| 11381961 | Not Issued | 20 | 05/05/2006 | Orthodontic Plate and Method | BECKENDORF, BRANDON G. |

Inventor Search Completed: No Records to Display.

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**Inventor Name Search Result**

Your Search was:

Last Name = SMITH
First Name = SHAWN

| Application# | Patent# | Status | Date Filed | Title | Inventor Name |
|---|---|---|---|---|---|
| 09192164 | 6185707 | 150 | 11/13/1998 | IC TEST SOFTWARE SYSTEM FOR MAPPING LOGICAL FUNCTIONAL TEST DATA OF LOGIC INTEGRATED CIRCUITS TO PHYSICAL REPRESENTATION | SMITH, SHAWN |
| 10998187 | Not Issued | 30 | 11/26/2004 | Two-component, rectifying-junction memory element | SMITH, SHAWN |
| 11229440 | Not Issued | 20 | 09/07/2005 | Pointing device and method of using same | SMITH, SHAWN |
| 60073442 | Not Issued | 159 | 02/02/1998 | SYSTEM AND METHOD FOR PROVIDING VOICE MESSAGING SERVICE UTILIZING A NETWORK CONNECTION | SMITH, SHAWN |
| 60442812 | Not Issued | 159 | 01/28/2003 | Add-a-drain | SMITH, SHAWN |
| 60468488 | Not Issued | 159 | 05/07/2003 | Sealed tank low flush toilet | SMITH, SHAWN |
| 60525056 | Not Issued | 159 | 11/25/2003 | Simplified, low switching voltage organic-on-inorganic diode memory element utilizing a conductive polymer fuse on a doped Si substrate | SMITH, SHAWN |
| 60652512 | Not Issued | 159 | 02/14/2005 | Add-a-drain | SMITH, SHAWN |
| 09663095 | 6428412 | 150 | 09/15/2000 | GAMING MACHINE WITH INTERLINKED ARRANGEMENTS OF PUZZLE ELEMENTS | SMITH, SHAWN A. |
| 10266308 | Not Issued | 61 | 10/08/2002 | Configuration representation and modeling using configuration spaces | SMITH, SHAWN A. P. |
| 10798615 | Not Issued | 30 | 03/11/2004 | Method and system for generating comparison of demand and supply data with high resolution capabilities | SMITH, SHAWN A. P. |
| 10827078 | Not Issued | 30 | 04/19/2004 | Consolidation of product data models | SMITH, SHAWN A. P. |
| 10404891 | Not Issued | 41 | 03/31/2003 | Configuration model consistency checking using flexible rule space subsets | SMITH, SHAWN A.P. |
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| 11276970 | Not Issued | 19 | 01/01/0001 | Edible Glue Stick for Cats | SMITH, SHAWN B |
| 10194920 | 6965895 | 150 | 07/12/2002 | METHOD AND APPARATUS FOR ANALYZING MANUFACTURING DATA | SMITH, SHAWN B. |
| 60305256 | Not Issued | 159 | 07/16/2001 | Automated method for using unsupervised neural networks for discovering and ranking data correlations in an unknow data set | SMITH, SHAWN B. |
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| 60308124 | Not Issued | 159 | 07/30/2001 | System and method for efficient management, reference, and extraction of large quantities of un-structured relational data | SMITH, SHAWN B. |





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| 60308125 | Not Issued | 159 | 07/30/2001 | Central control application for flexible branched data mining and statistical analysis for the purpose of automated exploration of statistical comparisons in unknown data sets | SMITH, SHAWN B. |
| 60309787 | Not Issued | 159 | 08/06/2001 | Fast statistical scoring and ranking method for correlating numbers to categories or attributes (e.g. Tool Ids). | SMITH, SHAWN B. |
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| 10286029 | Not Issued | 30 | 11/01/2002 | Method of ordering pharmaceutical and vaccine products | SMITH, SHAWN C. |
| 60336003 | Not Issued | 159 | 11/01/2001 | Method of ordering pharmaceutical and vaccine products | SMITH, SHAWN C. |
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| 60702163 | Not Issued | 20 | 07/25/2005 | Headwear with integral hydration reservoir | SMITH, SHAWN M. |
| 60510001 | Not Issued | 159 | 10/09/2003 | Invisivent | SMITH, SHAWN MARTIN |
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| 07796528 | 5223125 | 250 | 11/22/1991 | OXYGEN SENSOR FOR ALUMINUM KILLED, HIGH SILICON STEEL MELTS | SMITH, SHAWN P. |
| 09177202 | 6544210 | 150 | 10/22/1998 | DISPOSABLE LAPAROSCOPIC SMOKE EVACUATION SYSTEM | SMITH, SHAWN P. |
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| 10604452 | Not Issued | 30 | 07/22/2003 | Speaker-Buffer Management for Voice-Over-Internet-Protocol (VoIP) Triggered by Microphone-Buffer Arrival | SMITH, SHAWN W. |
| 07806763 | 5267322 | 150 | 12/13/1991 | DIGITAL AUTOMATIC GAIN CONTROL WITH LOOKAHEAD, ADAPTIVE NOISE FLOOR SENSING, AND DECAY BOOST INITIALIZATION | SMITH, SHAWN W. |

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www.cs.unb.ca/~gdueck/courses/cs4835/bdd97.pdf

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Software configuration management - 12:43pm
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1 Research track paper: On mining cross-graph quasi-cliques
Jian Pei, Daxin Jiang, Aidong Zhang
August 2005 **Proceeding of the eleventh ACM SIGKDD international conference on Knowledge discovery in data mining KDD '05**
**Publisher:** ACM Press
Full text available: pdf(573.85 KB) Additional Information: full citation, abstract, references, index terms

Joint mining of multiple data sets can often discover interesting, novel, and reliable patterns which cannot be obtained solely from any single source. For example, in cross-market customer segmentation, a group of customers who behave similarly in multiple markets should be considered as a more coherent and more reliable cluster than clusters found in a single market. As another example, in bioinformatics, by joint mining of gene expression data and protein interaction data, we can find cluster ...

**Keywords:** bioinformatics, graph mining, patterns

2 Session 10A: Approximating the list-chromatic number and the chromatic number in minor-closed and odd-minor-closed classes of graphs
Ken-ichi Kawarabayashi, Bojan Mohar
May 2006 **Proceedings of the thirty-eighth annual ACM symposium on Theory of computing STOC '06**
**Publisher:** ACM Press
Full text available: pdf(339.51 KB) Additional Information: full citation, abstract, references, index terms

It is well-known (Feige and Kilian [24], Håstad [39]) that approximating the chromatic number within a factor of $n^{1-\epsilon}$ cannot be done in polynomial time for $\epsilon>0$, unless coRP = NP. Computing the list-chromatic number is much harder than determining the chromatic number. It is known that the problem of deciding if the list-chromatic number is k, where $k \geq 3$, is $\Pi_2^p$-complete [37].In this paper, we focus on minor-closed and odd-minor-close ...

**Keywords:** Hadwiger conjecture, graph coloring, graph minor, list coloring, odd-minor

3 A framework for call graph construction algorithms
David Grove, Craig Chambers
November 2001 **ACM Transactions on Programming Languages and Systems (TOPLAS)**, Volume 23 Issue 6
**Publisher:** ACM Press
Full text available: pdf(1.36 MB) Additional Information: full citation, abstract, references, citings, index terms

A large number of call graph construction algorithms for object-oriented and functional languages have been proposed, each embodying different tradeoffs between analysis cost and call graph precision. In this article we present a unifying framework for understanding call graph construction algorithms and an empirical comparison of a representative set of algorithms. We first present a general parameterized algorithm that encompasses many well-known and novel call graph construction algorithms. W ...

**Keywords:** Call graph construction, control flow analysis, interprocedural analysis

4 Coloring k-colorable graphs using smaller palettes
Eran Halperin, Ram Nathaniel, Uri Zwick
January 2001 **Proceedings of the twelfth annual ACM-SIAM symposium on Discrete algorithms**
**Publisher:** Society for Industrial and Applied Mathematics
Full text available: pdf(574.16 KB) Additional Information: full citation, abstract, references, citings, index terms

We obtain the following new coloring results:

- A 3-colorable graph on $n$ vertices with maximum degree &Dgr; can be colored, in polynomial time, using $\tilde{O}((\Delta \log \Delta)^{1/3} \cdot \log n)$ colors. This slightly improves an $\tilde{O}((\Delta^{1/3} \log^{1/6} \Delta) \cdot \log n)$ bound given by Karger, Motwani and Sudan. More generally, $k$-colorable graphs with maximum degree &Dgr; can be colored, in polynomial ...

5 The power of a pebble: exploring and mapping directed graphs
Michael A. Bender, Antonio Fernández, Dana Ron, Amit Sahai, Salil Vadhan
May 1998 **Proceedings of the thirtieth annual ACM symposium on Theory of computing**
**Publisher:** ACM Press
Full text available: pdf(1.47 MB) Additional Information: full citation, references, citings, index terms

6 Oral session 2: web searching and applications: Multi-graph enabled active learning for multimodal web image retrieval
Xin-Jing Wang, Wei-Ying Ma, Lei Zhang, Xing Li
November 2005 **Proceedings of the 7th ACM SIGMM international workshop on Multimedia information retrieval MIR '05**
**Publisher:** ACM Press
Full text available: pdf(371.23 KB) Additional Information: full citation, abstract, references, index terms

In this paper, we propose a multimodal Web image retrieval technique based on multi-graph enabled active learning. The main goal is to leverage the heterogeneous data on the Web to improve retrieval precision. Three graphs are constructed on images' content features, textual annotations and hyperlinks respectively, namely Content-Graph, Text-Graph and Link-Graph, which provide complimentary information on the images. By analyzing the three graphs, a training dataset is automatically created and ...

**Keywords:** active learning, graph learning, mjultimodal image retrieval

Terms used **correcting DAG** Found **32,010** of **178,880**

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1 The principled design of large-scale recursive neural network architectures--dag-rnns and the protein structure prediction problem
Pierre Baldi, Gianluca Pollastri
December 2003 **The Journal of Machine Learning Research**, Volume 4
**Publisher:** MIT Press
Full text available: pdf(231.40 KB) Additional Information: full citation, abstract, references, index terms

We describe a general methodology for the design of large-scale recursive neural network architectures (DAG-RNNs) which comprises three fundamental steps: (1) representation of a given domain using suitable directed acyclic graphs (DAGs) to connect visible and hidden node variables; (2) parameterization of the relationship between each variable and its parent variables by feedforward neural networks; and (3) application of weight-sharing within appropriate subsets of DAG connections to capture s ...

2 The weakest failure detector for solving consensus
Tushar Deepak Chandra, Vassos Hadzilacos, Sam Toueg
July 1996 **Journal of the ACM (JACM)**, Volume 43 Issue 4
**Publisher:** ACM Press
Full text available: pdf(770.03 KB) Additional Information: full citation, abstract, references, citings, index terms

We determine what information about failures is necessary and sufficient to solve Consensus in asynchronous distributed systems subject to crash failures. In Chandra and Toueg [1996], it is shown that W, a failure detector that provides surprisingly little information about which processes have crashed, is sufficient to solve Consensus in asynchronous systems with a majority of correct processes. In this paper, we prove that to solve Consensus, any failure detector has to p ...

**Keywords:** Byzantine Generals' problem, agreement problem, asynchronous systems, atomic broadcast, commit problem, consensus problem, crash failures, failure detection, fault-tolerance, message passing, partial synchrony, processor failures

3 Symbolic Debugging of Optimized Code
John Hennessy
July 1982 **ACM Transactions on Programming Languages and Systems (TOPLAS)**, Volume 4 Issue 3
**Publisher:** ACM Press





Full text available: pdf(1.37 MB) Additional Information: full citation, references, citings, index terms

4 Incremental analysis of real programming languages
Tim A. Wagner, Susan L. Graham
May 1997 **ACM SIGPLAN Notices , Proceedings of the ACM SIGPLAN 1997 conference on Programming language design and implementation PLDI '97**, Volume 32 Issue 5
**Publisher:** ACM Press
Full text available: pdf(1.95 MB) Additional Information: full citation, abstract, references, citings, index terms

A major research goal for compilers and environments is the automatic derivation of tools from formal specifications. However, the formal model of the language is often inadequate; in particular, LR($k$) grammars are unable to describe the natural syntax of many languages, such as C++ and Fortran, which are inherently non-deterministic. Designers of batch compilers work around such limitations by combining generated components with ad hoc techniques (for instance, performing part ...

5 Locking Primitives in a Database System
Henry F. Korth
January 1983 **Journal of the ACM (JACM)**, Volume 30 Issue 1
**Publisher:** ACM Press
Full text available: pdf(1.61 MB) Additional Information: full citation, references, citings, index terms

6 Shrinking the warehouse update Window
Wilburt Juan Labio, Ramana Yerneni, Hector Garcia-Molina
June 1999 **ACM SIGMOD Record , Proceedings of the 1999 ACM SIGMOD international conference on Management of data SIGMOD '99**, Volume 28 Issue 2
**Publisher:** ACM Press
Full text available: pdf(1.34 MB) Additional Information: full citation, abstract, references, citings, index terms

Warehouse views need to be updated when source data changes. Due to the constantly increasing size of warehouses and the rapid rates of change, there is increasing pressure to reduce the time taken for updating the warehouse views. In this paper we focus on reducing this "update window" by minimizing the work required to compute and install a batch of updates. Various strategies have been proposed in the literature for updating a single warehouse view. These algorithms typically ...

7 Resilience of general interactive tasks
Benny Chor, Lee-Bath Nelson
August 1994 **Proceedings of the thirteenth annual ACM symposium on Principles of distributed computing**
**Publisher:** ACM Press
Full text available: pdf(977.65 KB) Additional Information: full citation, references, index terms

8 Automatic generation of DAG parallelism
R. Cytron, M. Hind, W. Hsieh
June 1989 **ACM SIGPLAN Notices , Proceedings of the ACM SIGPLAN 1989 Conference on Programming language design and implementation PLDI '89**, Volume 24 Issue 7
**Publisher:** ACM Press

Terms used **DAG combination** Found **54,583** of **178,880**

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1 Combinational logic synthesis for LUT based field programmable gate arrays
Jason Cong, Yuzheng Ding
April 1996 **ACM Transactions on Design Automation of Electronic Systems (TODAES)**, Volume 1 Issue 2
**Publisher:** ACM Press
Full text available: pdf(628.91 KB) Additional Information: full citation, abstract, references, citings, index terms, review

The increasing popularity of the field programmable gate-array (FPGA) technology has generated a great deal of interest in the algorithmic study and tool development for FPGA-specific design automation problems. The most widely used FPGAs are LUT based FPGAs, in which the basic logic element is a K-input one-output lookup-table (LUT) that can implement any Boolean function of up to K variables. This unique feature of the LUT has brought new challenges to lo ...

**Keywords:** FPGA, area minimization, computer-aided design of VLSI, decomposition, delay minimization, delay modeling, logic optimization, power minimization, programmable logic, routing, simplification, synthesis, system design, technology mapping

2 Delay-optimal technology mapping by DAG covering
Yuji Kukimoto, Robert K. Brayton, Prashant Sawkar
May 1998 **Proceedings of the 35th annual conference on Design automation**
**Publisher:** ACM Press
Full text available: pdf(200.73 KB) Publisher Site Additional Information: full citation, abstract, references, citings, index terms

We propose an algorithm for minimal-delay technology mapping for library-based designs. We show that subject graphs need not be decomposed into trees for delay minimization; they can be mapped directly as DAGs. Experimental results demonstrate that significant delay improvement is possible by this new approach.

**Keywords:** congestion, global routing, quadratic placement, relaxed pins, routing models, supply-demand

3 The principled design of large-scale recursive neural network architectures--dag-rnns





and the protein structure prediction problem
Pierre Baldi, Gianluca Pollastri
December 2003 **The Journal of Machine Learning Research**, Volume 4
**Publisher:** MIT Press
Full text available: pdf(231.40 KB) Additional Information: full citation, abstract, references, index terms

We describe a general methodology for the design of large-scale recursive neural network architectures (DAG-RNNs) which comprises three fundamental steps: (1) representation of a given domain using suitable directed acyclic graphs (DAGs) to connect visible and hidden node variables; (2) parameterization of the relationship between each variable and its parent variables by feedforward neural networks; and (3) application of weight-sharing within appropriate subsets of DAG connections to capture s ...

4 A Method of Test Generation fo Path Delay Faults Using Stuck-at Fault Test Generation Algorithms
Satoshi Ohtake, Kouhei Ohtani, Hideo Fujiwara
March 2003 **Proceedings of the conference on Design, Automation and Test in Europe - Volume 1 DATE '03**
**Publisher:** IEEE Computer Society
Full text available: pdf(158.22 KB) Publisher Site Additional Information: full citation, abstract, index terms

In this paper, we propose a test generation method for non-robust path delay faults using stuck-at fault test generation algorithms. In our method, we first transform an original combinational circuit into a circuit called a partial leaf-dag using path-leaf transformation. Then we generate test patterns using a stuck-at fault test generation algorithm for stuck-at faults in the partial leaf-dag. Finally we transform the test patterns into two-pattern tests for path delay faults in the original c ...

5 Research session: XML query processing #4: Structure and content scoring for XML
Sihem Amer-Yahia, Nick Koudas, Amélie Marian, Divesh Srivastava, David Toman
August 2005 **Proceedings of the 31st international conference on Very large data bases VLDB '05**
**Publisher:** VLDB Endowment
Full text available: pdf(937.09 KB) Additional Information: full citation, abstract, references, index terms

XML repositories are usually queried both on structure and content. Due to structural heterogeneity of XML, queries are often interpreted approximately and their answers are returned ranked by scores. Computing answer scores in XML is an active area of research that oscillates between pure content scoring such as the well-known *tf*idf* and taking structure into account. However, none of the existing proposals *fully accounts for structure* and *combines it with content* to score ...

6 An efficient algorithm for finding the minimal-area FPGA technology mapping
Chi-Chou Kao, Yen-Tai Lai
January 2005 **ACM Transactions on Design Automation of Electronic Systems (TODAES)**, Volume 10 Issue 1
**Publisher:** ACM Press
Full text available: pdf(231.76 KB) Additional Information: full citation, abstract, references, index terms

Minimum area is one of the important objectives in technology mapping for lookup table-based field-progrmmable gate arrays (FPGAs). Although there is an algorithm that can find an optimal solution in polynomial time for the minimal-area FPGA technology mapping problem without gate duplication, its time complexity can grow exponentially with the number of inputs of the lookup-tables. This article proposes an algorithm with approximate to the area-optimal solution and lower time complexity. The ti ...

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